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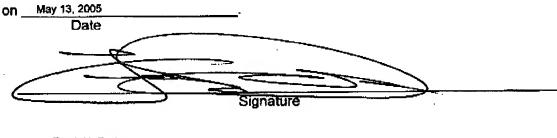
PTO/SB/97 (09-04)

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Applicants: Moghissi, et al. Filing Date: 04/14/2004

Serial No.: 10/824,061 Docket No.: CCT 106

Title: VOLTAGE MEASUREMENT WITH AUTOMATED CORRECTION FOR INPUT IMPEDANCE ERRORS

Appended is an information Disclosure Statement By Applicant (PTO/SB/08a (08-03)) for filing in the above-identified patent application.

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Substitute for 10th Property	Application Number	10/824,061			
INFORMATION DISCLOSURE	Filing Date	04/14/2004			
STATEMENT BY APPLICANT	First Named Inventor	Moghissi, et al.			
STATEMENT DI ALI EIVAN	Art Unit	2858			
(Use as meny sheets as necessary)	Examiner Name	(Not yet assigned)			
Sheet 1 of 1	Attorney Docket Number	CCT 106			

			U.S. PATENT	Name of Patentee or	Pages, Columns, Lines, Where
Initials' No.'	Document Number	Publication Date MM-DD-YYYY	Applicant of Cited Occurrent	Relevant Passages or Relevant	
	Number-Kind Code ^{2 (Floresco})			Figures Appear	
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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Foreign Patent Document Country Code ³ Number ⁴ Tond Code ³ (# known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Unes, Where Relevant Passages or Relevant Figures Appear	7*
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